

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10765481
<b>Filing Date:</b>	27-Jan-2004
<b>Title of Invention:</b>	Method and apparatus for a two-step resist soft bake to prevent ILD outgassing during semiconductor processing
<b>First Named Inventor/Applicant Name:</b>	Paul Shirley
<b>Filer:</b>	Robert A. Manware
<b>Attorney Docket Number:</b>	MICS:0117 (02-1051)

Filed as Large Entity

### **Utility under 35 USC 111(a) Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	810	810
<b>Total in USD (\$)</b>				<b>940</b>